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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/614,307	07/02/2003	Hemanshu D. Bhatt	03-0524	8241
24319	7590	07/12/2005	EXAMINER	
LSI LOGIC CORPORATION			GUERRERO, MARIA F	
1621 BARBER LANE				
MS: D-106			ART UNIT	PAPER NUMBER
MILPITAS, CA 95035			2822	

DATE MAILED: 07/12/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

PA

Office Action Summary	Application No.	Applicant(s)	
	10/614,307	BHATT ET AL.	
	Examiner Maria Guerrero	Art Unit 2822	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 25 April 2005.

2a) This action is FINAL. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-30 is/are pending in the application.

4a) Of the above claim(s) 1-15 and 28-30 is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) 16-27 is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some * c) None of:

1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892)

2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
 Paper No(s)/Mail Date _____

4) Interview Summary (PTO-413)
 Paper No(s)/Mail Date. _____

5) Notice of Informal Patent Application (PTO-152)

6) Other: _____

DETAILED ACTION

1. This Office Action is in response to the Election filed April 25, 2005.

Status of Claims

2. Claims 1-30 are pending.

Information Disclosure Statement

3. The information disclosure statement (IDS) submitted on July 2, 2003 has been considered.

Election/Restrictions

4. Applicant's election without traverse of Species 1A, claims 16-27 in the reply filed on April 25, 2005 is acknowledged.

Claims 1-15 and 28-30 are withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected invention, there being no allowable generic or linking claim. Election was made **without** traverse in the reply filed on April 25, 2005.

Specification

The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claims 1-2, 4-5, 7, and 18 are rejected under 35 U.S.C. 102(b) as being anticipated by Burghartz et al. (U.S. 5,793,272).

Burghartz et al. teaches an integrated circuit comprising: conductor layer arranged substantially parallel to a plane of the integrated circuit, insulating layers disposed between the conductor layers, and an inductor formed within the conductor layers and the insulating layer having magnetic flux lines substantially parallel to the plane of the integrated circuit upon operation of the integrated circuit (Abstract, Fig. 1-14, col. 1, lines 5-10, col. 4, lines 59-67, col. 5, lines 1-60, col. 8, lines 50-6, col. 9, lines 15-35). Burghartz et al. discloses the inductor having substantially coil shaped configuration (Abstract, Fig. 1). Burghartz et al. shows the inductor including a first segment (coil shaped) and a second segment (coil shaped) electrically connected to, proximate to and spaced apart from the first segment (Fig. 1-14).

In addition, Burghartz et al. teaches the integrated circuit comprising other electronic component disposed under conductor layer and insulating layers and electrical conductor disposed in the conductor layers between the first segment and

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second segment of the inductor and connecting the other electronic components (Fig. 1-14, col. 2, lines 40-67, col. 3, lines 1-65, col. 7, lines 3-35, col. 9, lines 5-15). Burghartz et al. discloses the inductor including a rectangular cross section with horizontal and vertical legs (Fig. 1-14). Burghartz et al. shows the inductor including a first and a second end proximate to the edge of the integrated circuit (Fig. 1-14).

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Maria Guerrero whose telephone number is 571-272-1837.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on 571-272-1852. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

July 8, 2005

Maria Guerrero
MARIA F. GUERRERO
PRIMARY EXAMINER